

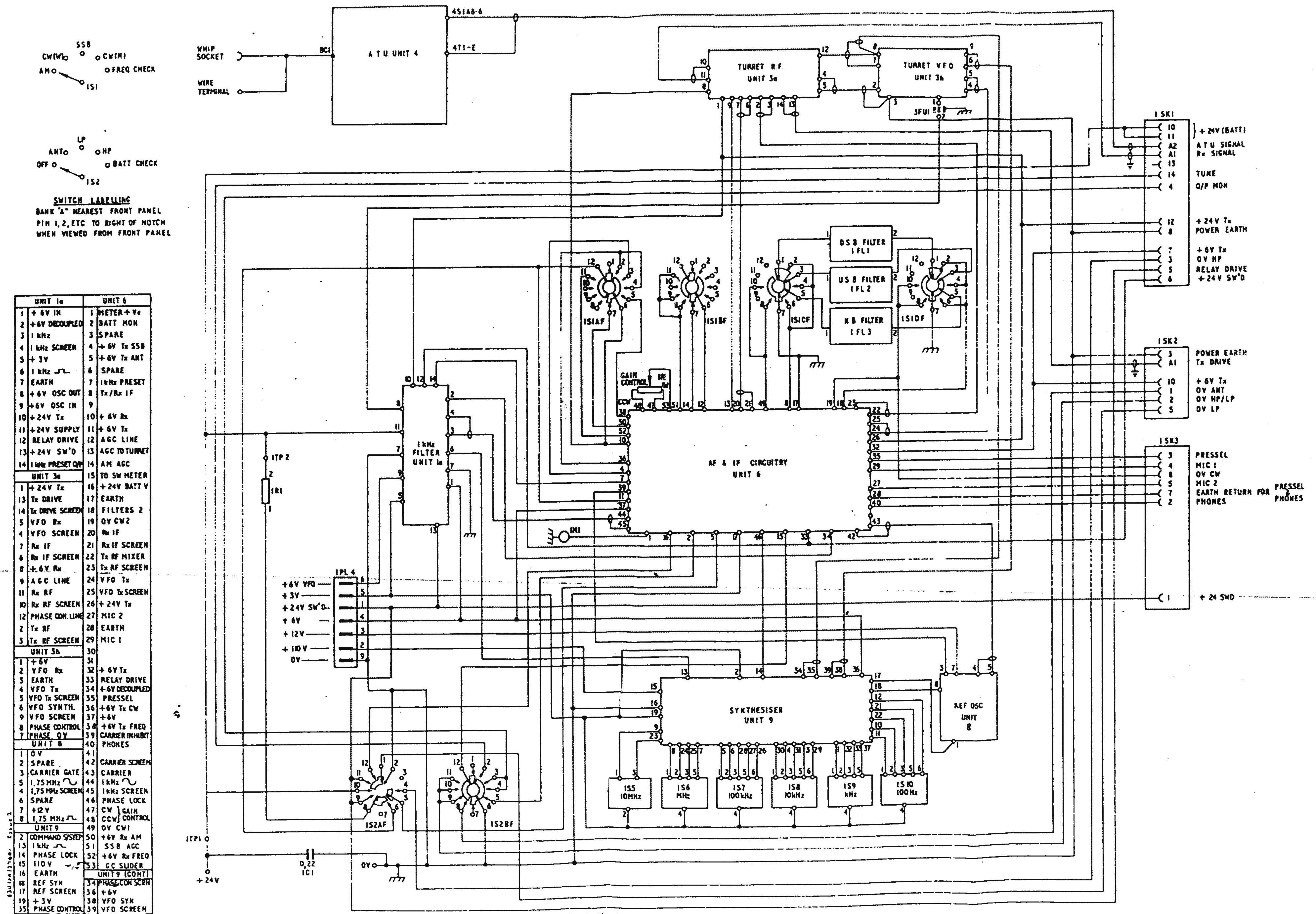


Fig.1

1551-2

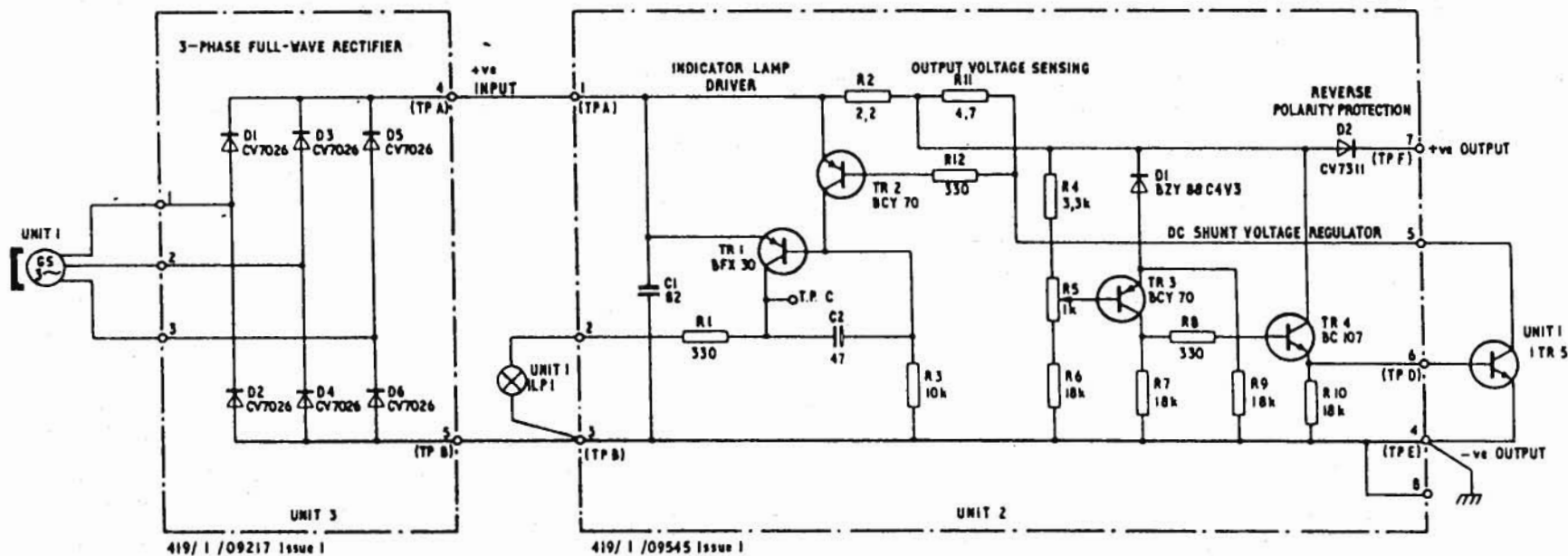
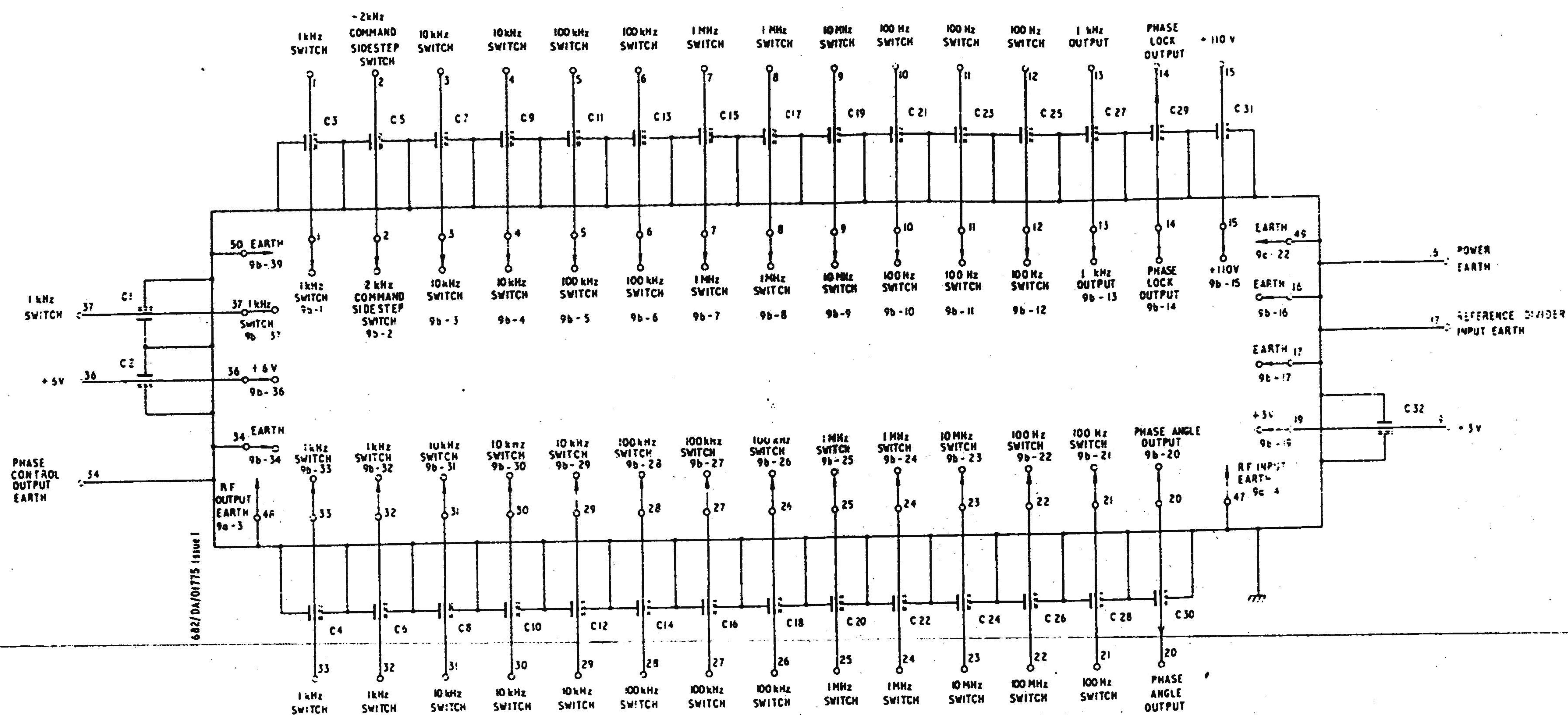
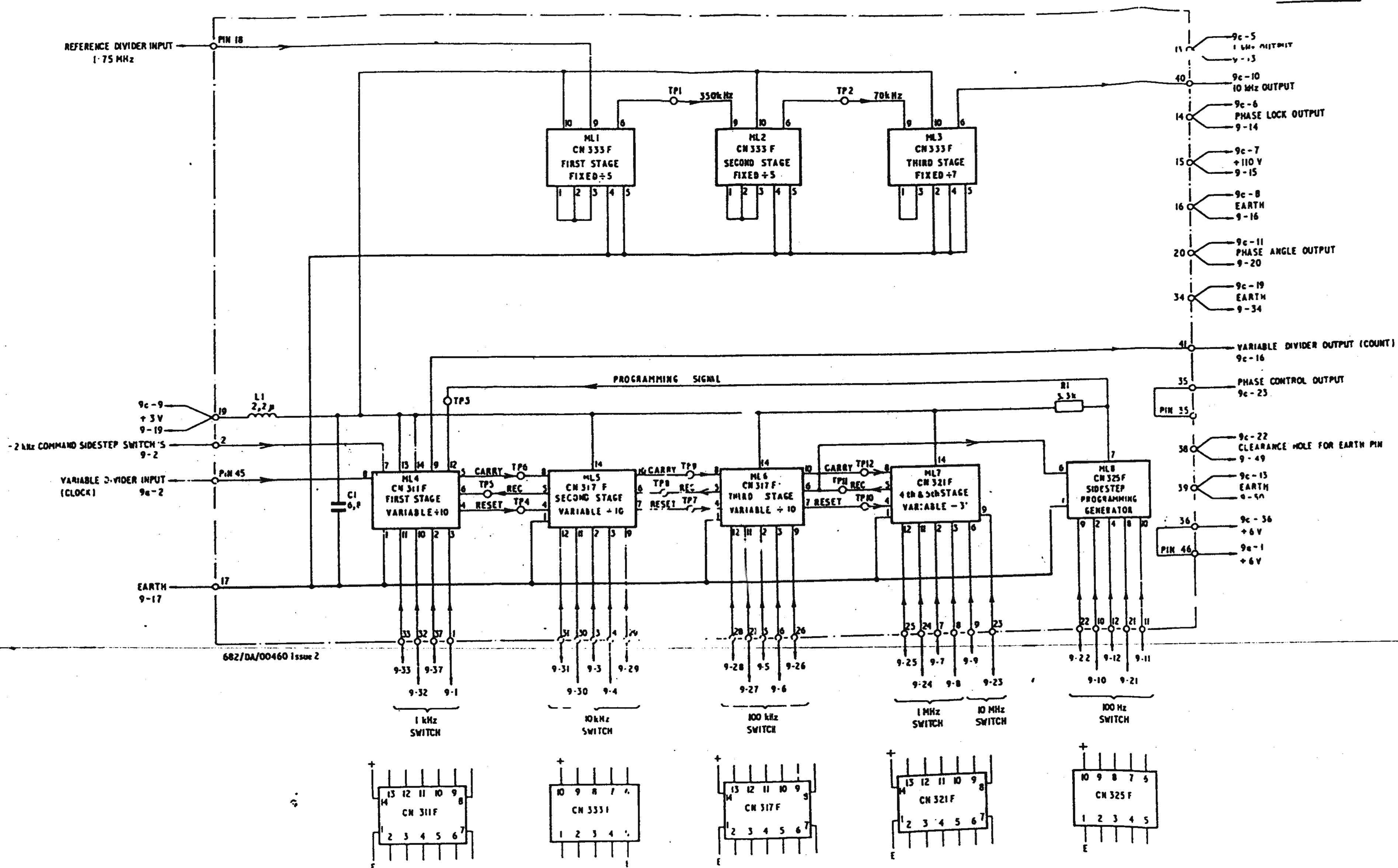


Fig.1 Units 1,2 and 3 - circuit diagram



Unit 9 synthesiser inputs and connections to internal units



Unit 9b-circuit

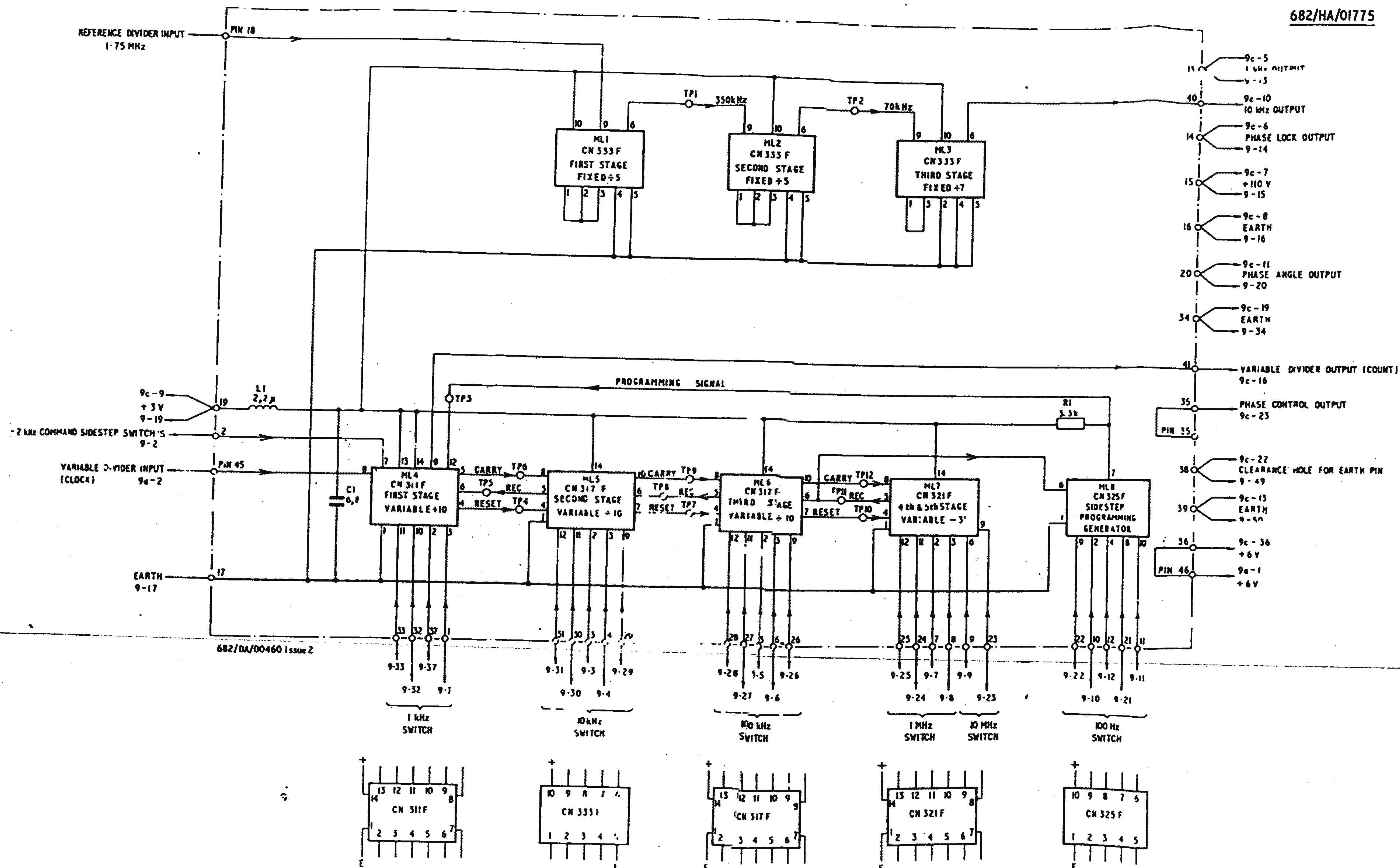


Fig. 3
Issue 1

Unit 9b-b-circuit

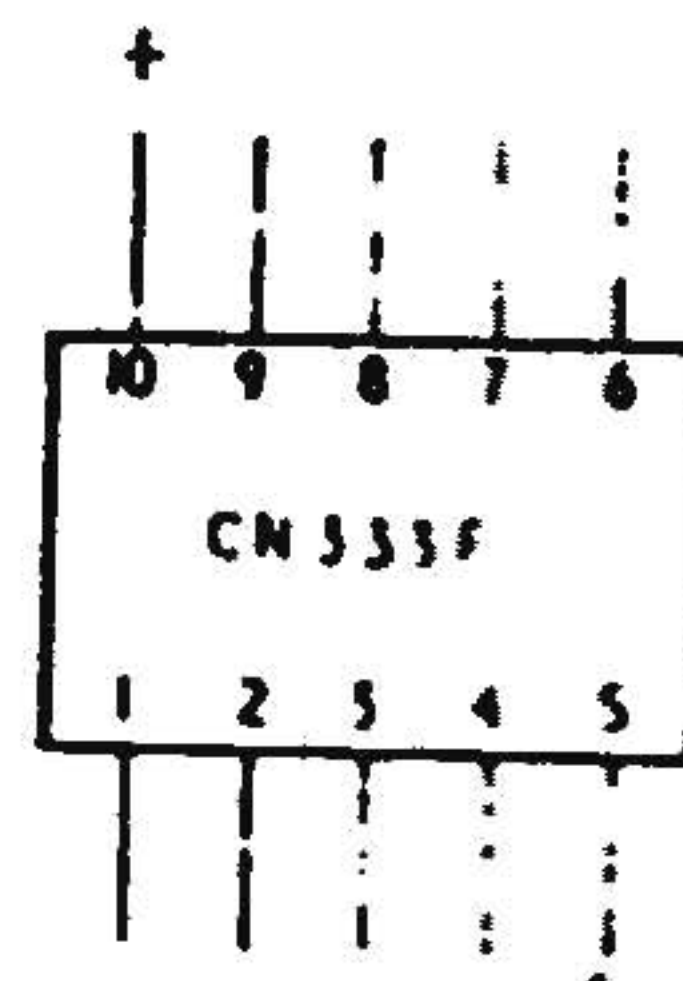
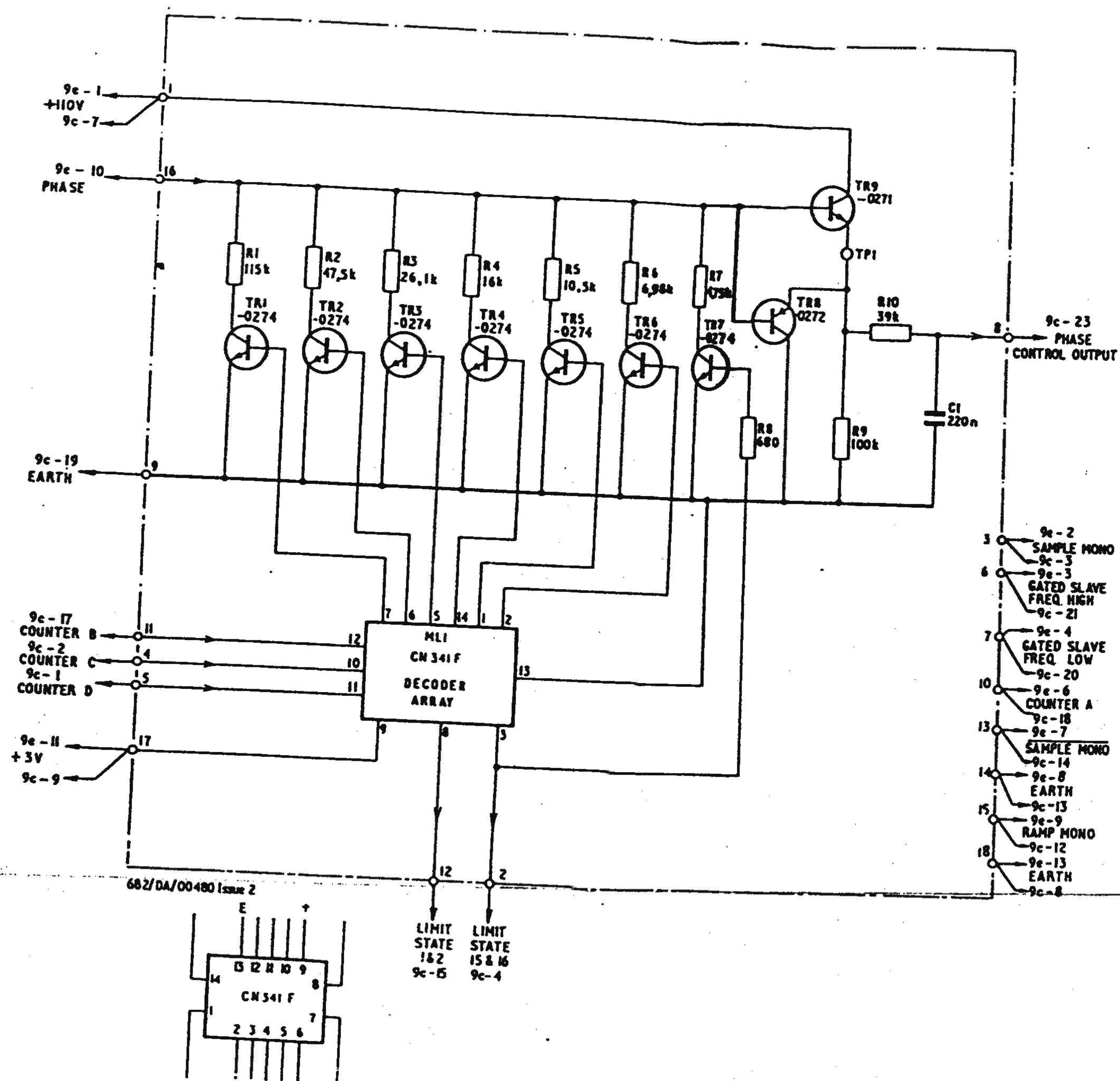


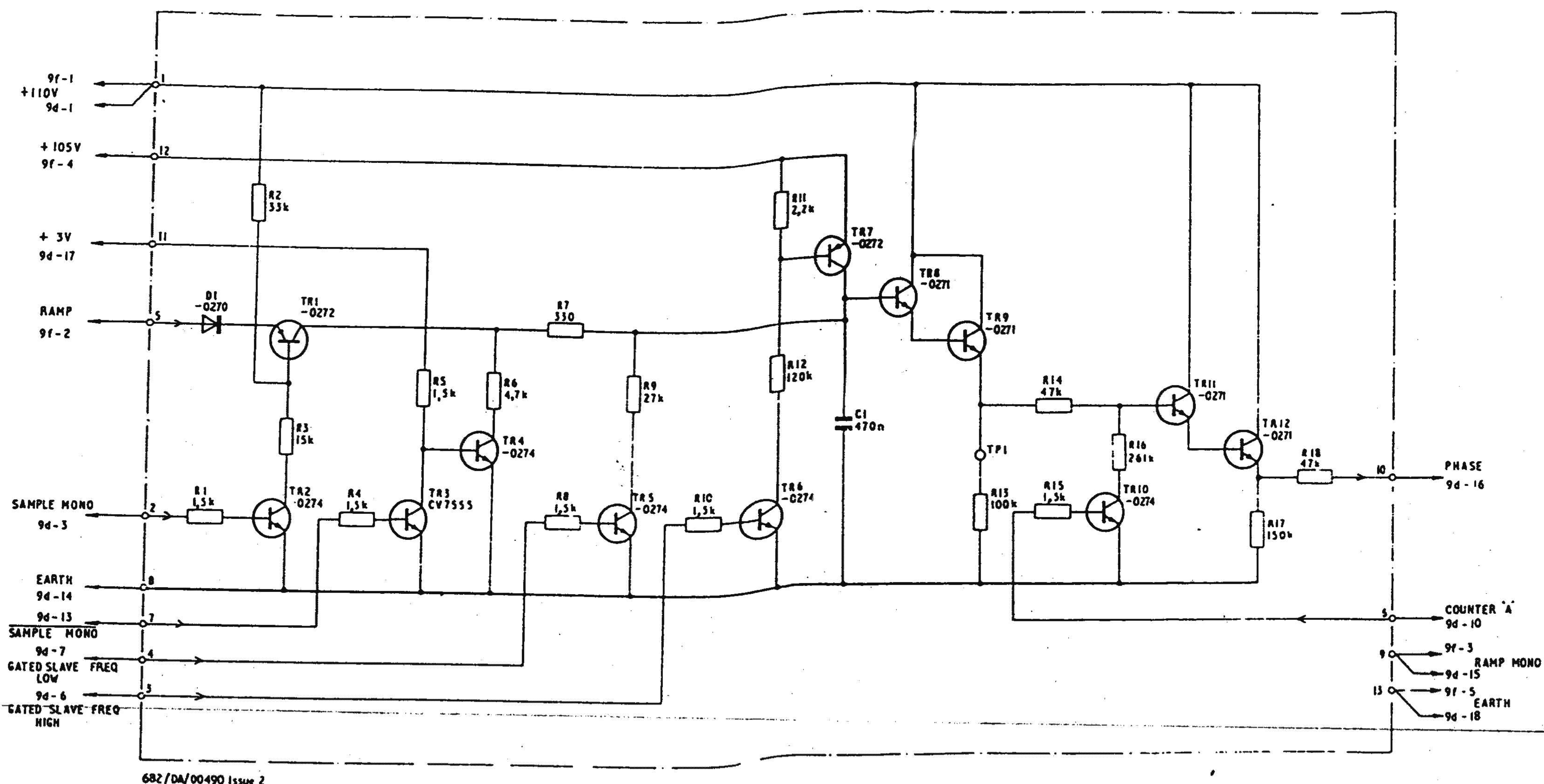
Fig. 4
Issue 1

Unit 9c - circuit

Fig. 4
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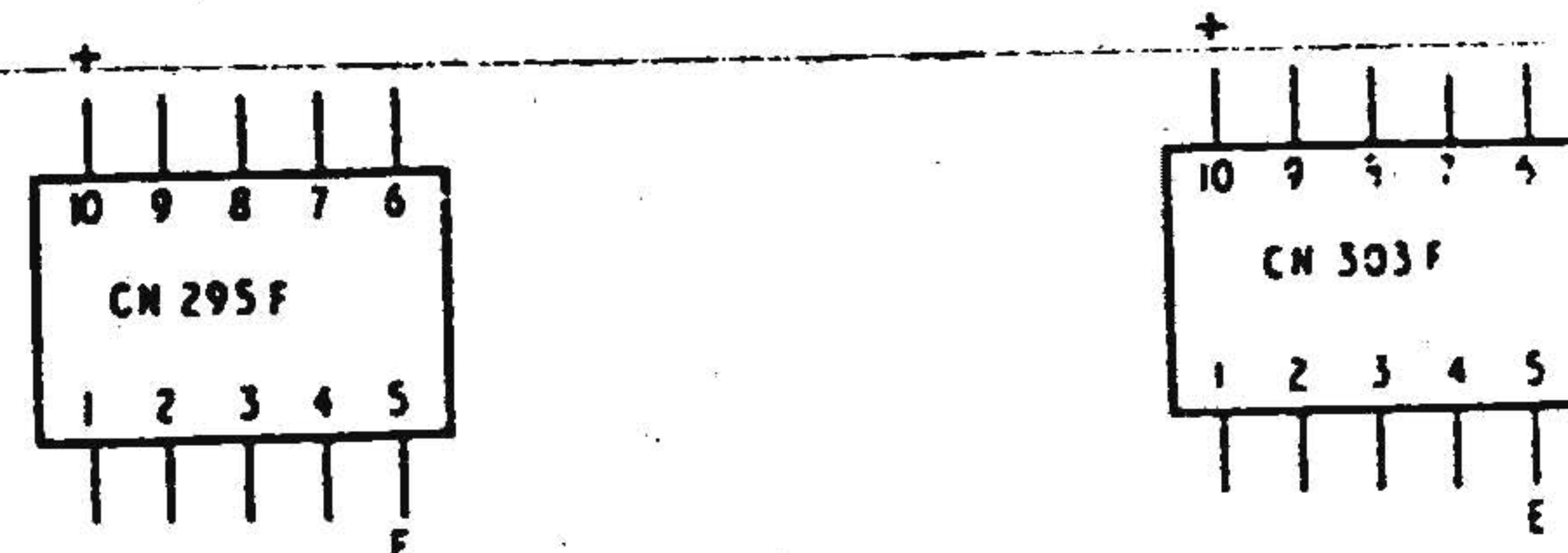
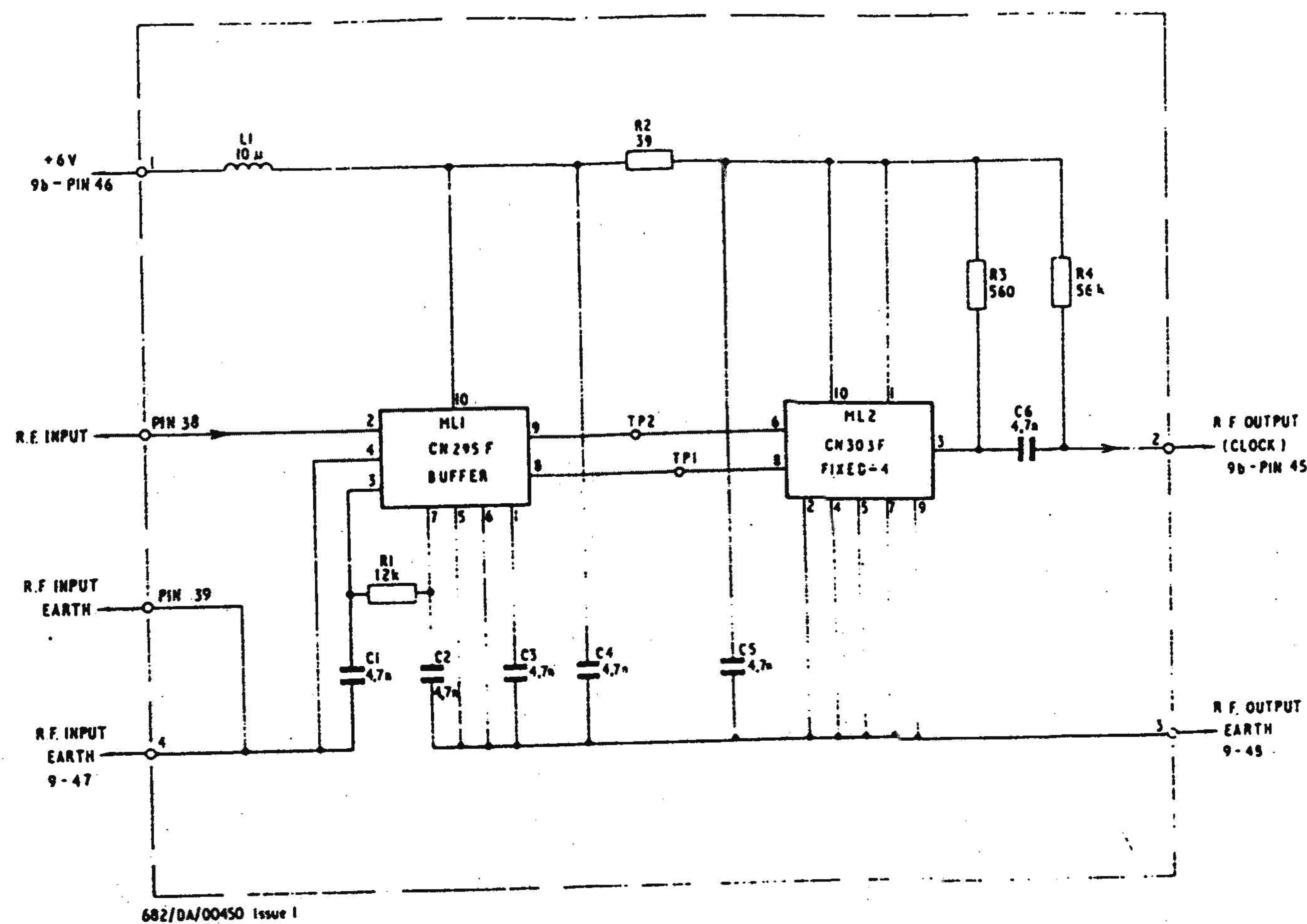


Unit 9d-circuit

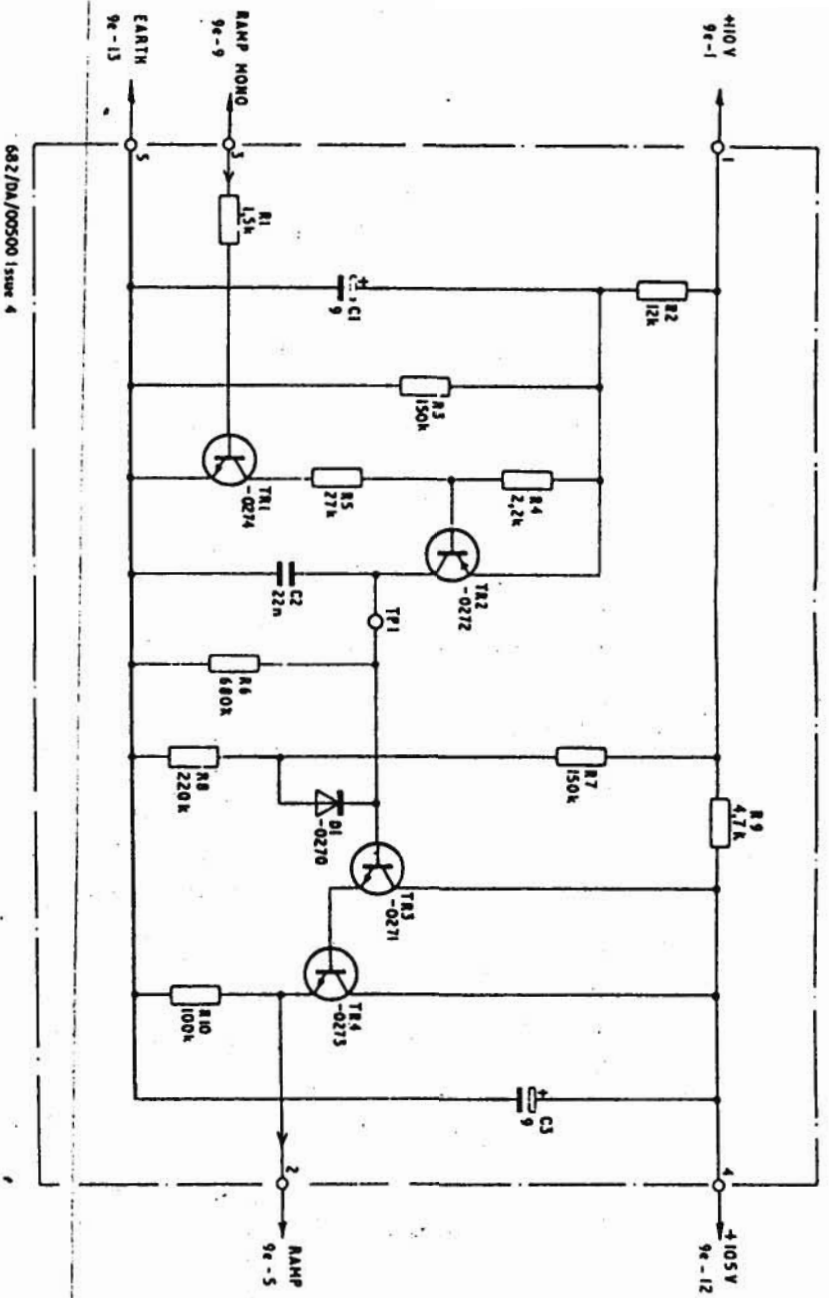
Fig. 6
Issue 1

Unit 9e - circuit

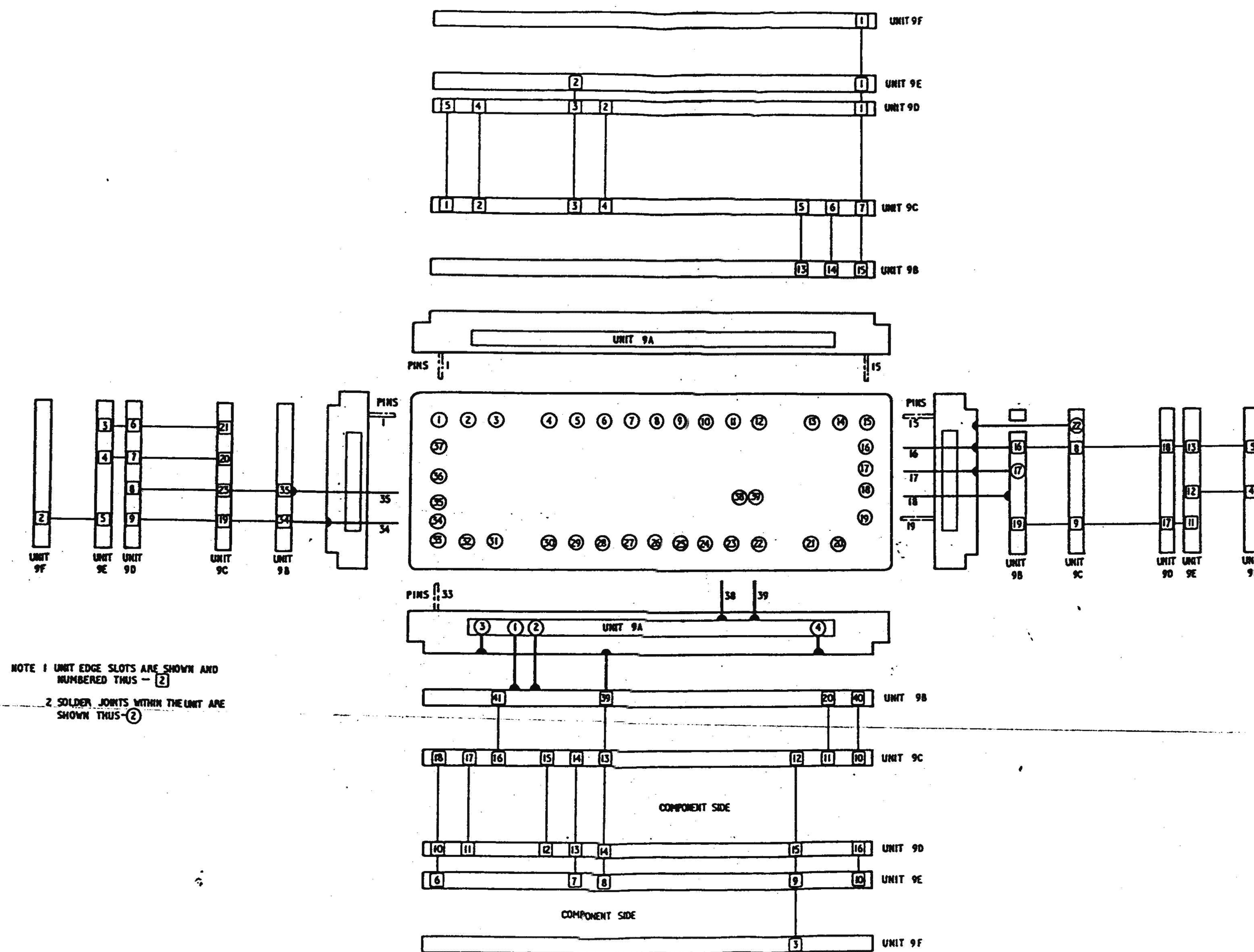
Fig. 6
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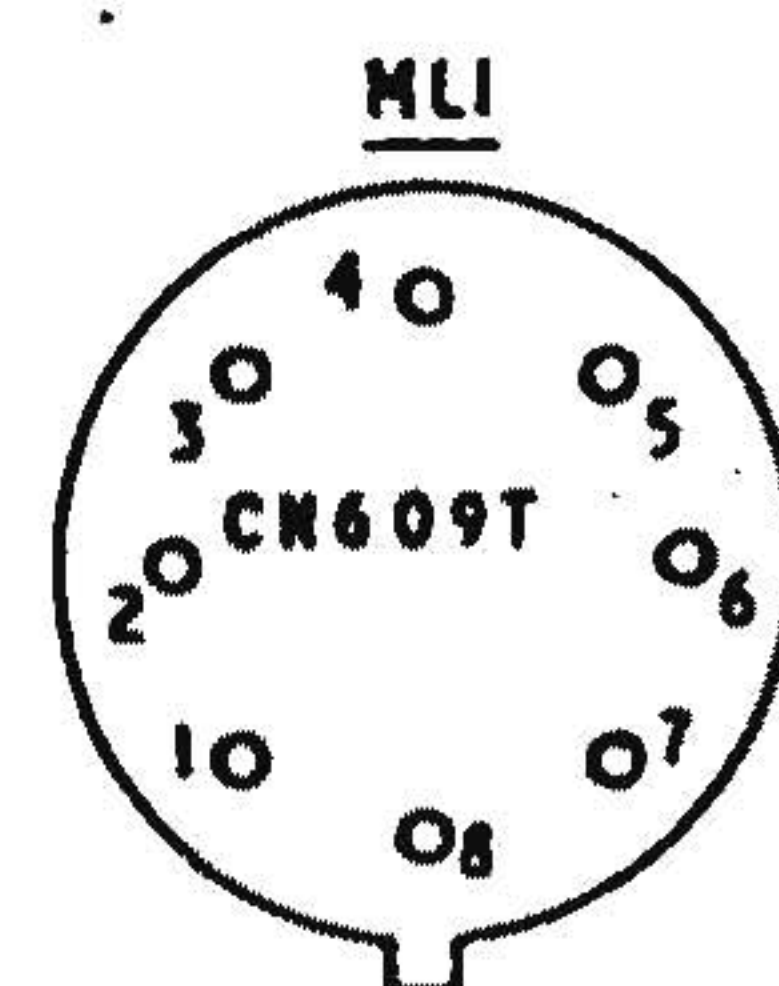
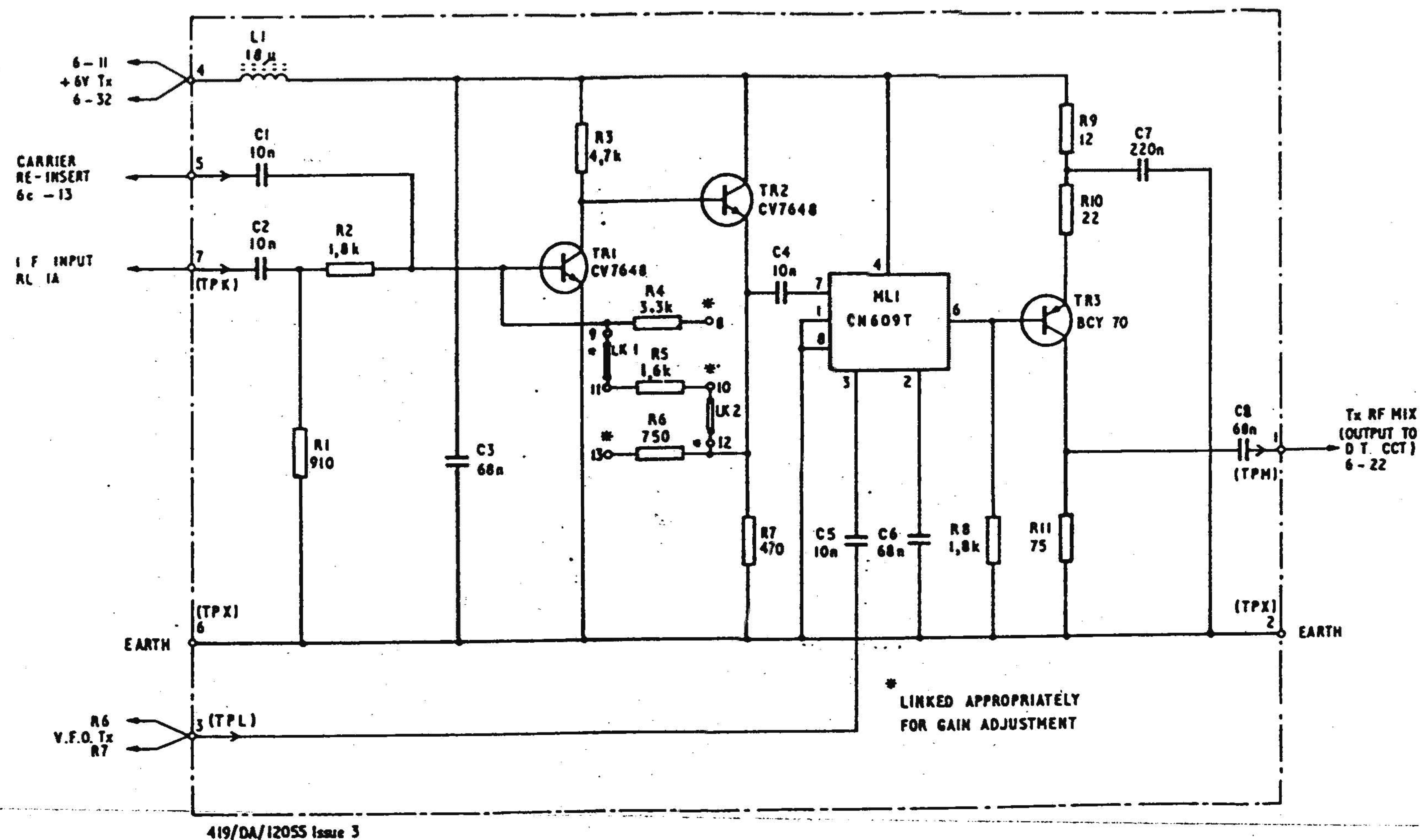
Unit 9a - circuit



Unit 9f - circuit

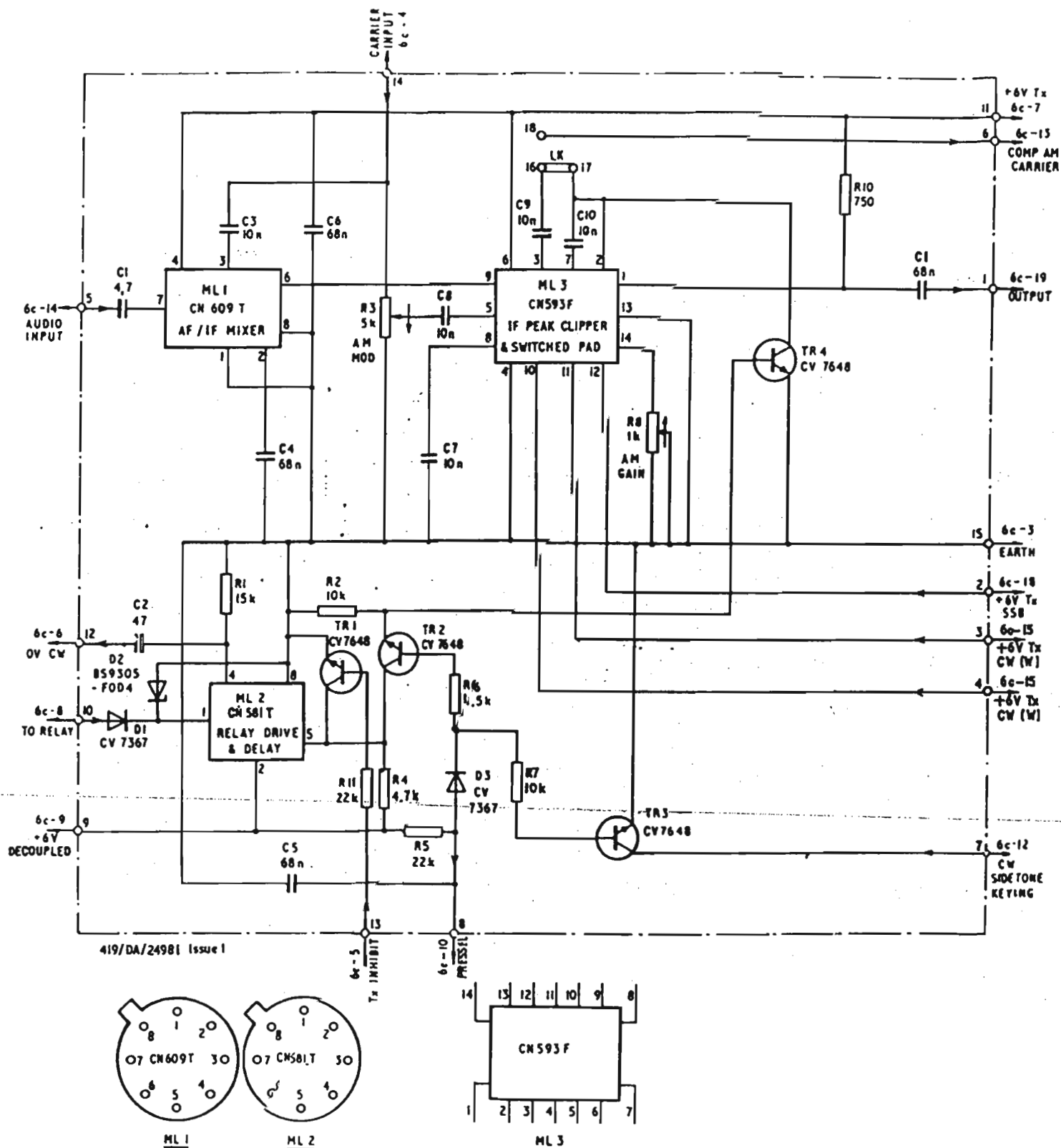


Synthesiser wire link - layout

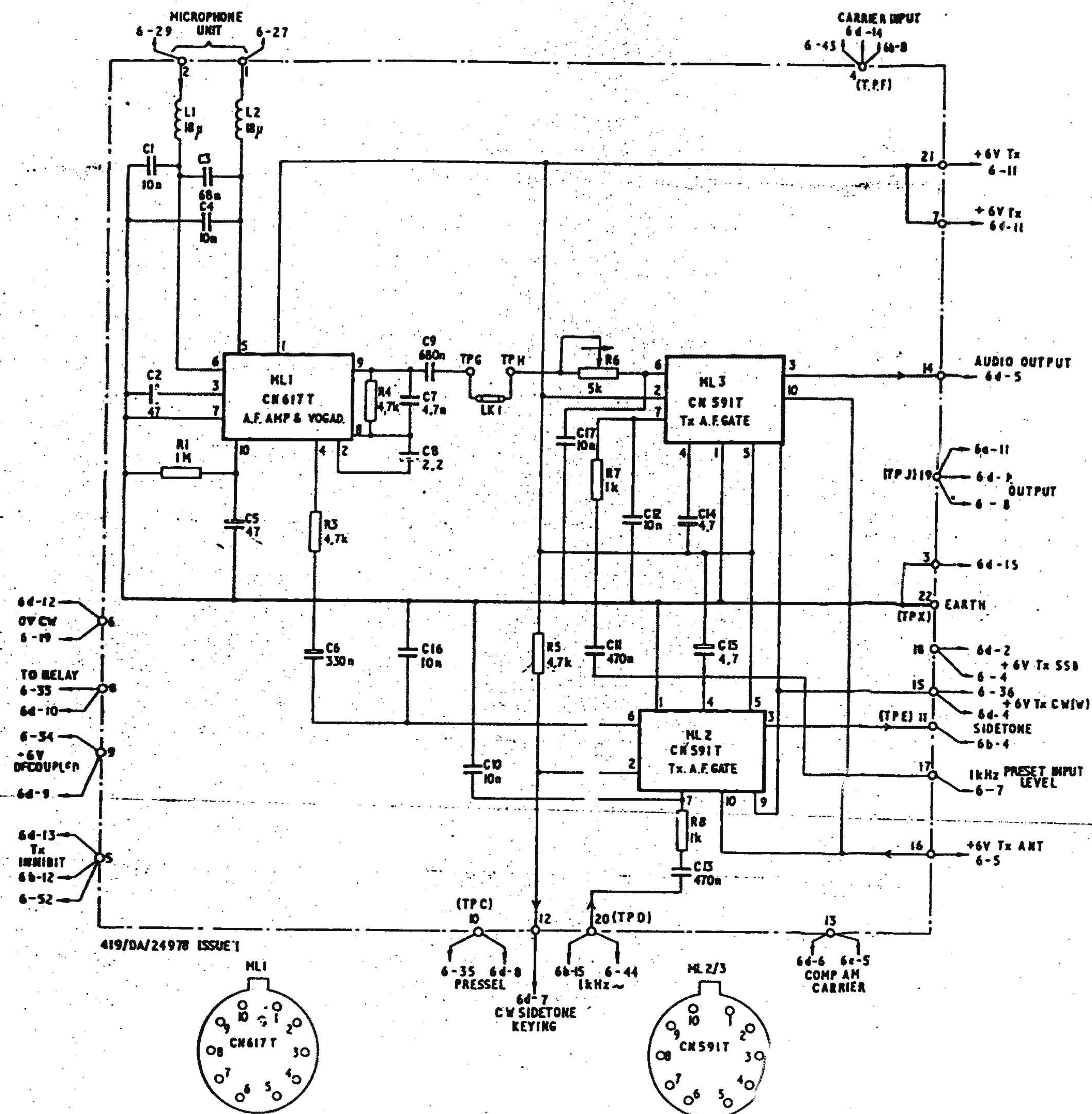


VIEW ON PIN ENDS

Transmitter IF/RF mixer - circuit



Unit 6d. transmitter AF to IF and relay drive circuit



Unit 6c. transmitter AF and gating - circuit

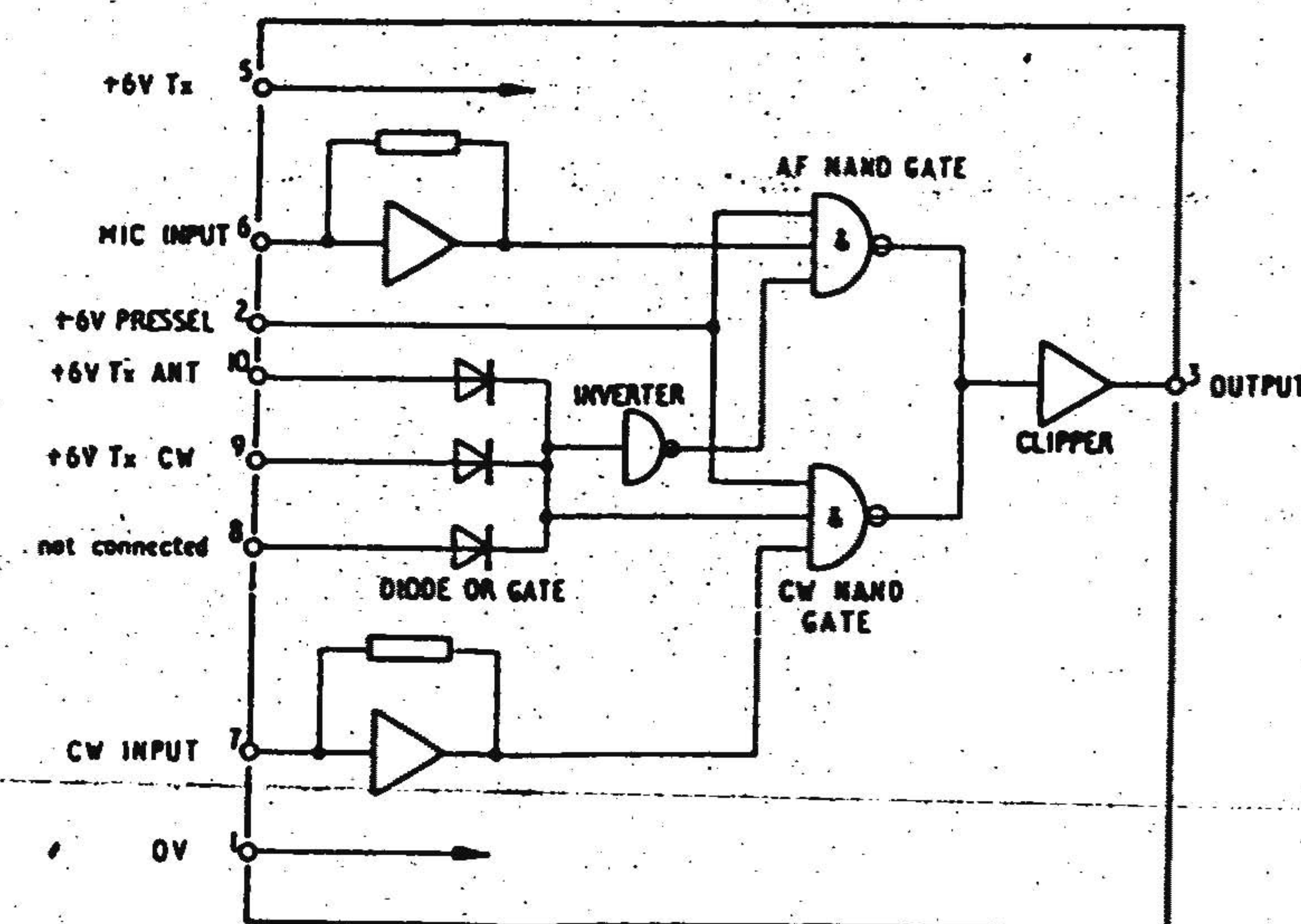
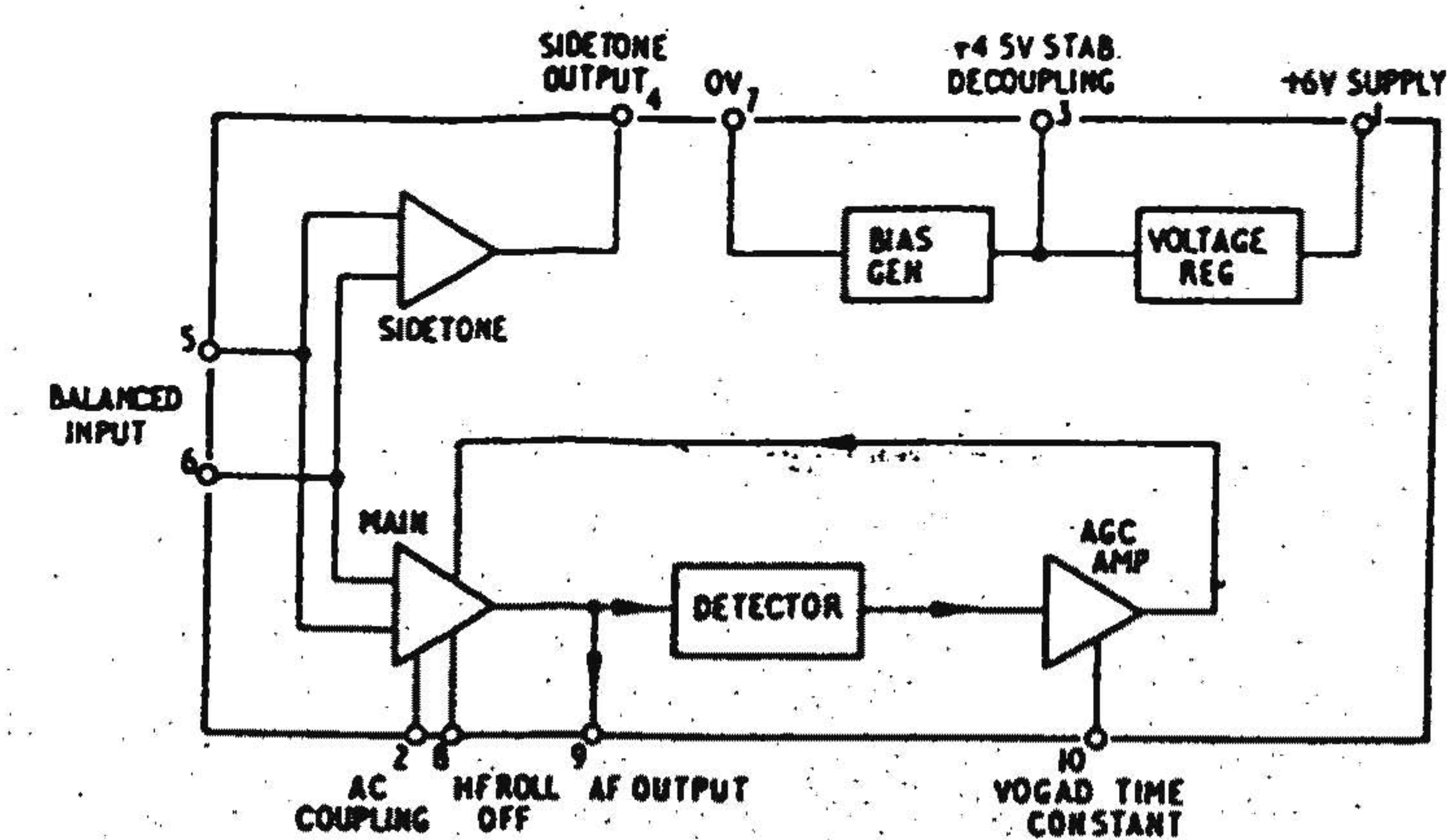


Fig. 1

Issue 1

Fig. 1

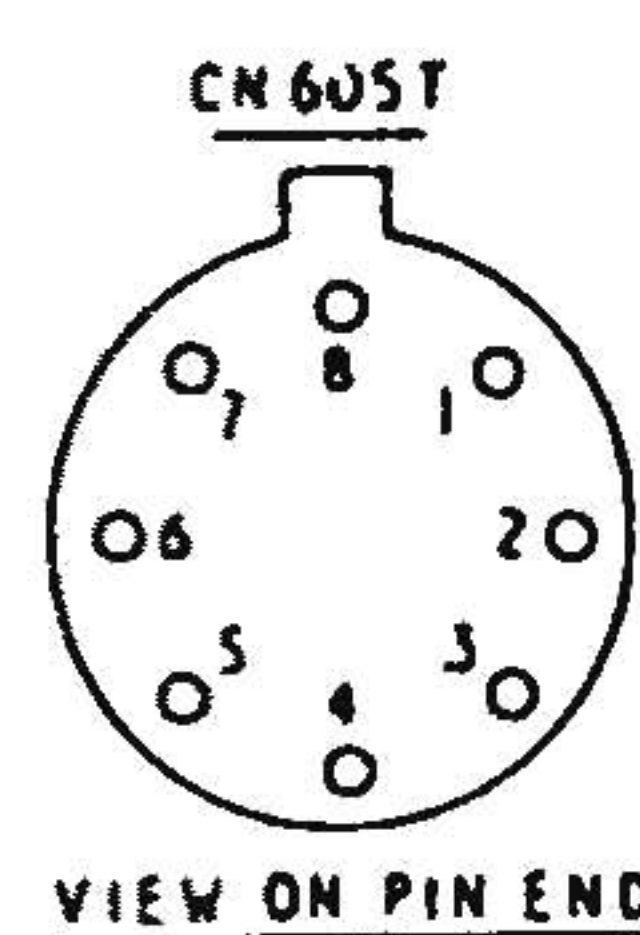
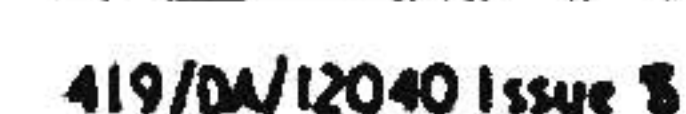
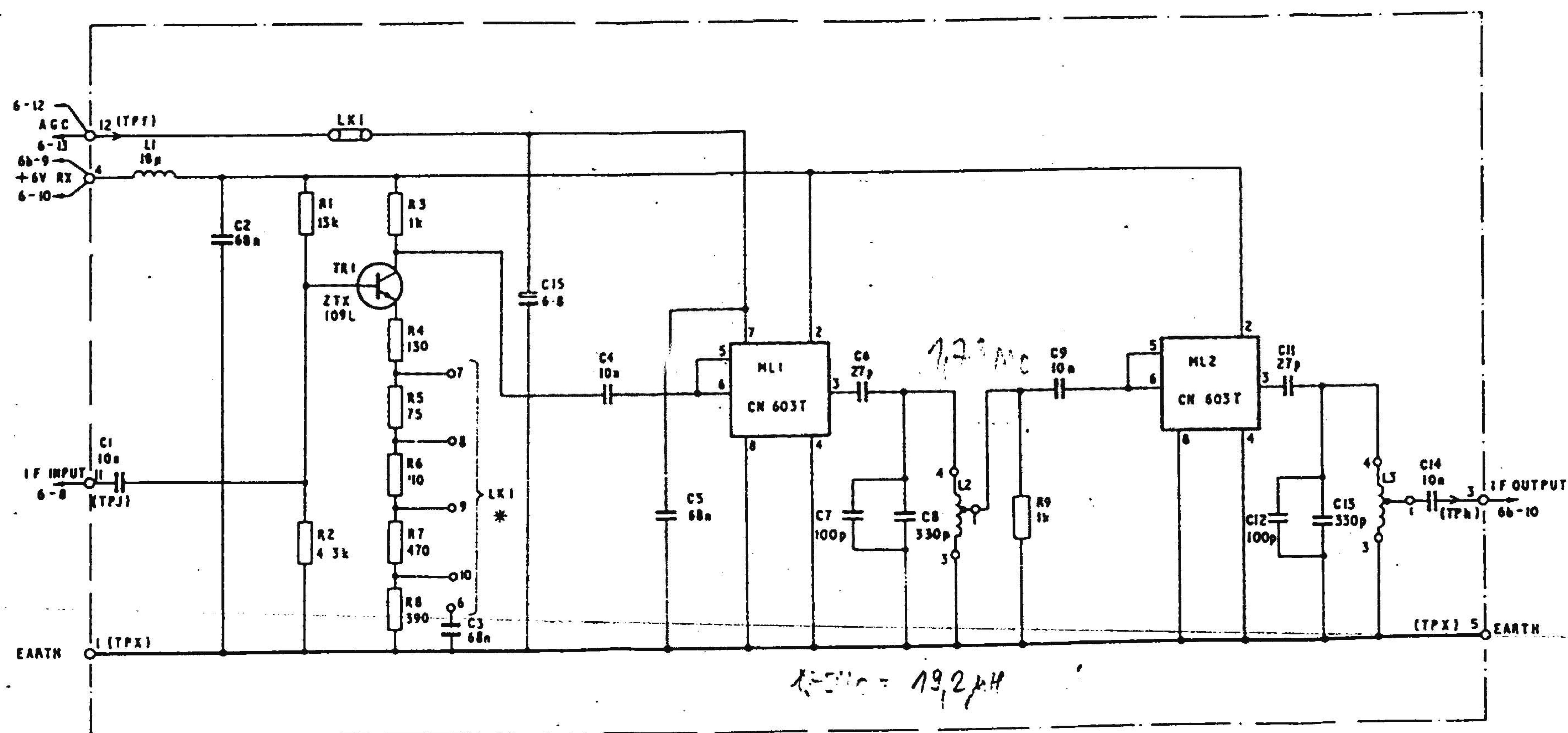


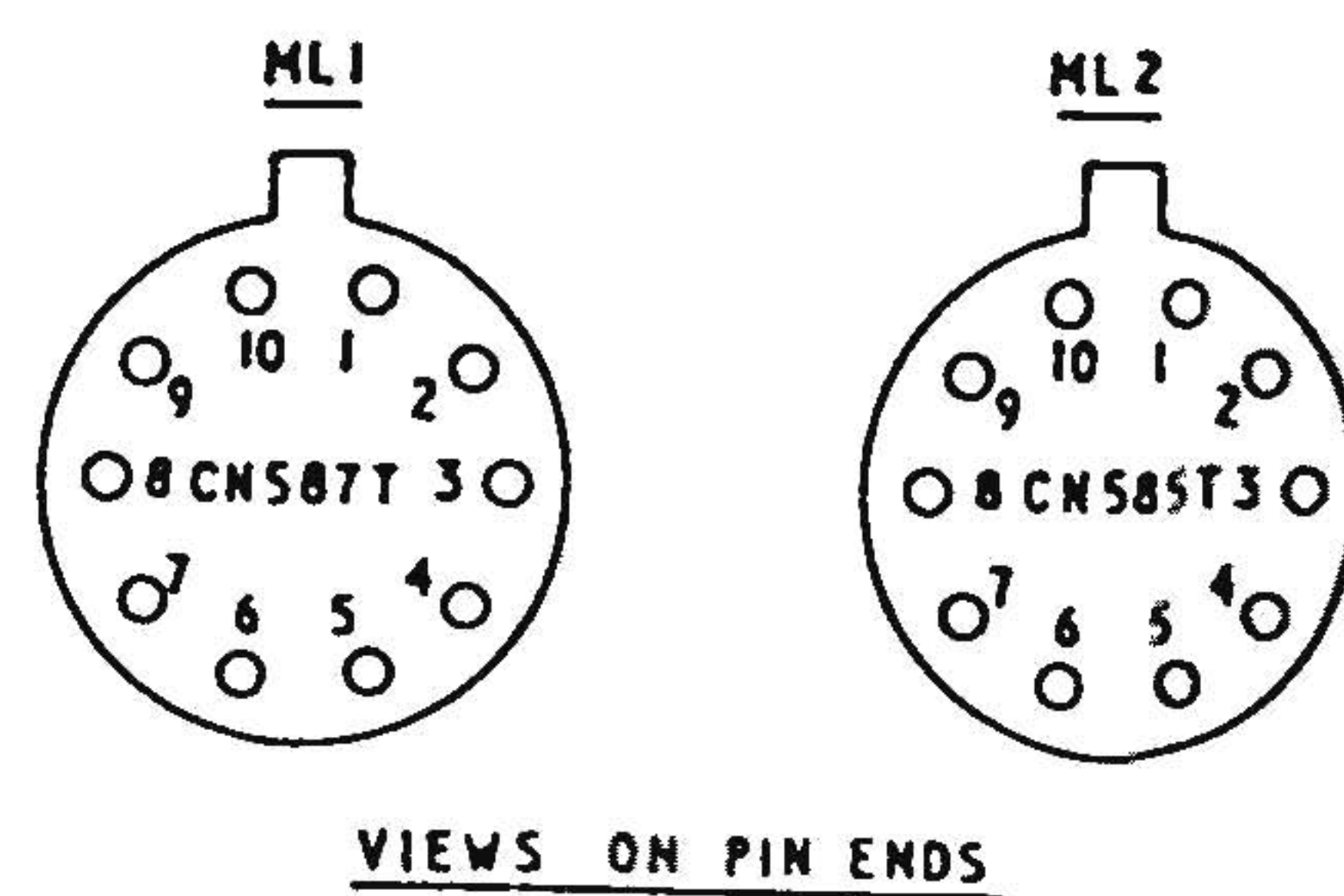
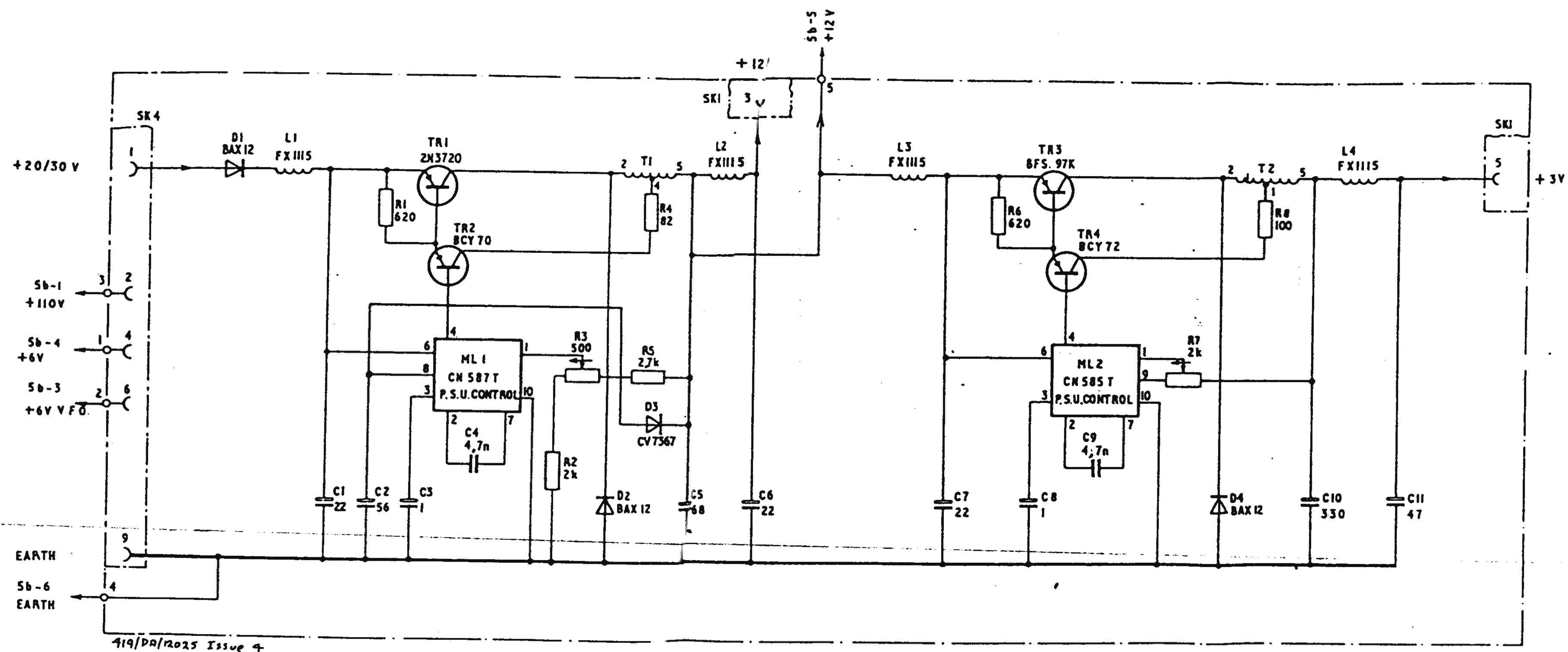
Fig.1
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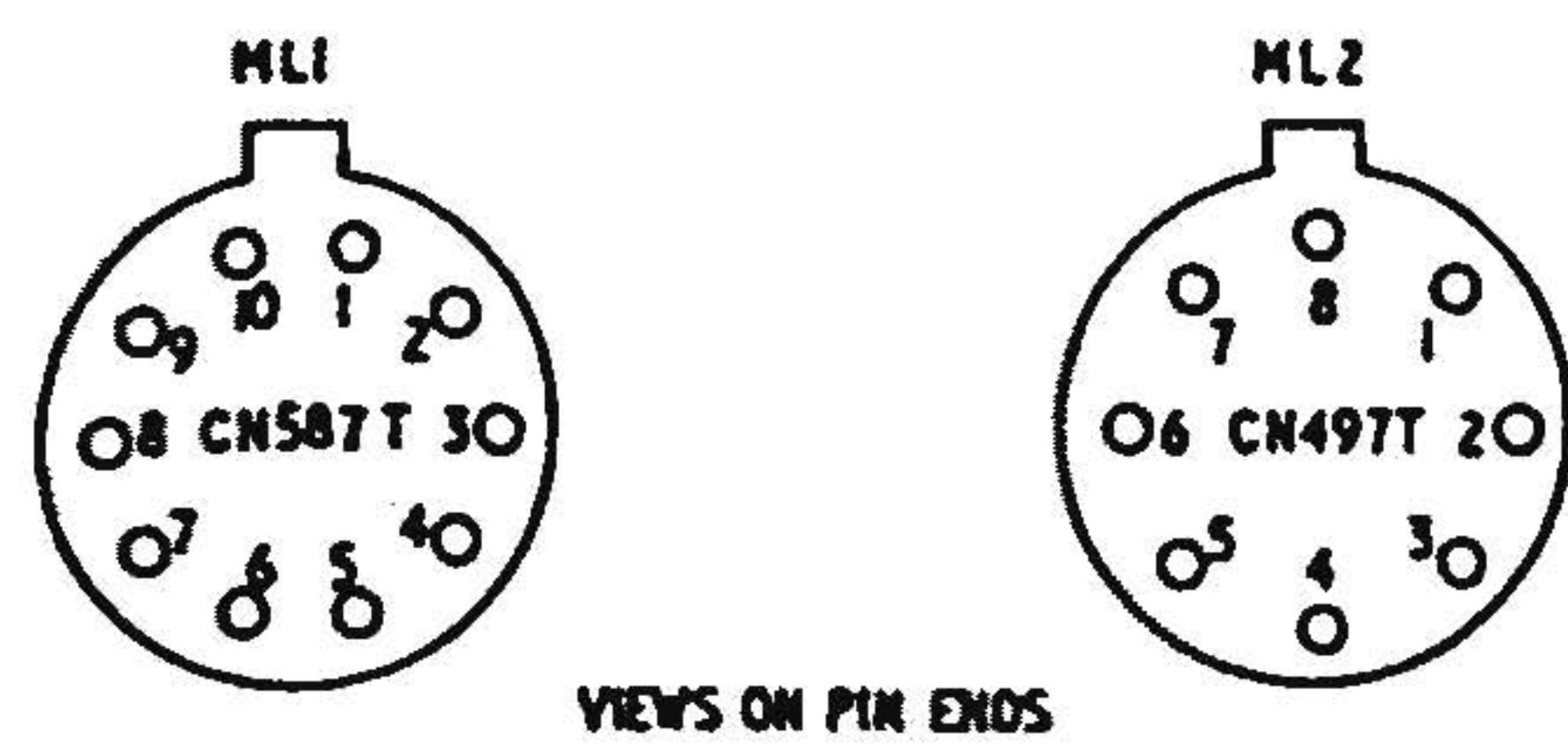
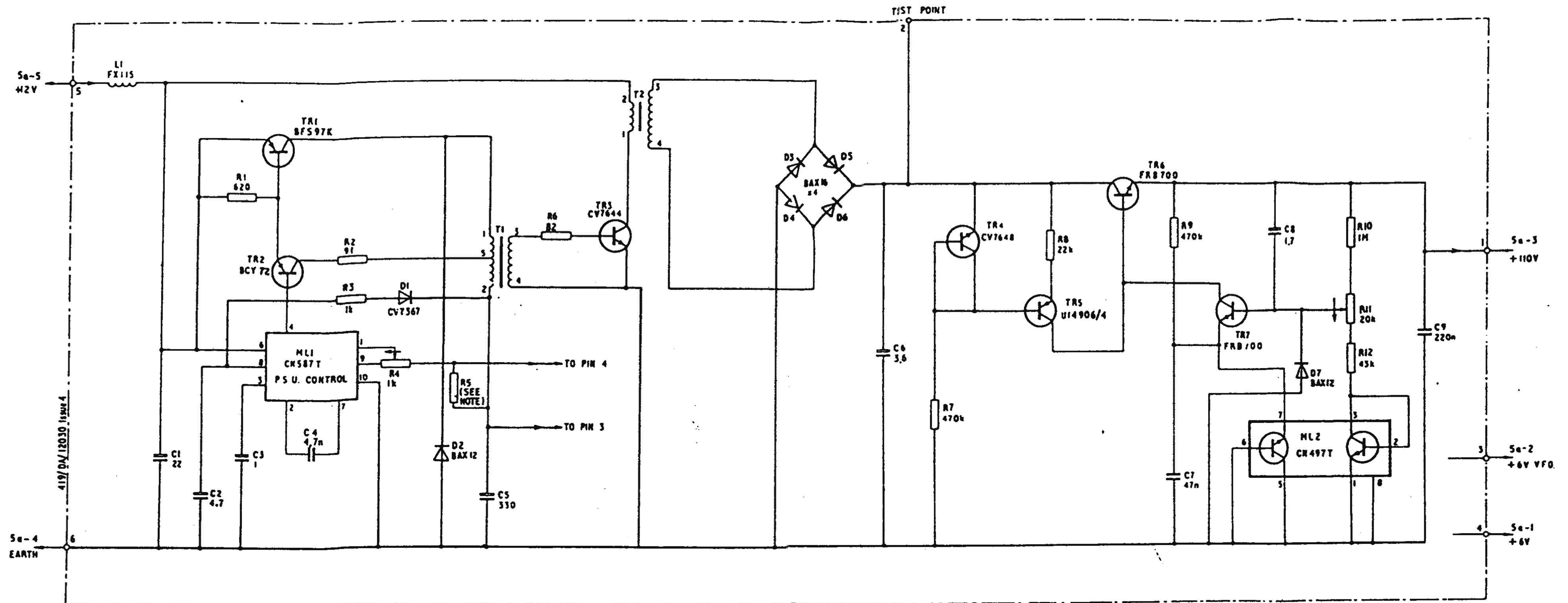
419/DA/12035 Issue 5

* LINKED APPROPRIATELY
FOR GAIN ADJUSTMENT.

Unit 6a, receiver IF stages circuit



Unit 5a, power supply +12V & +3V - circuit



Unit 5b power supply +6V & +110V-circuit

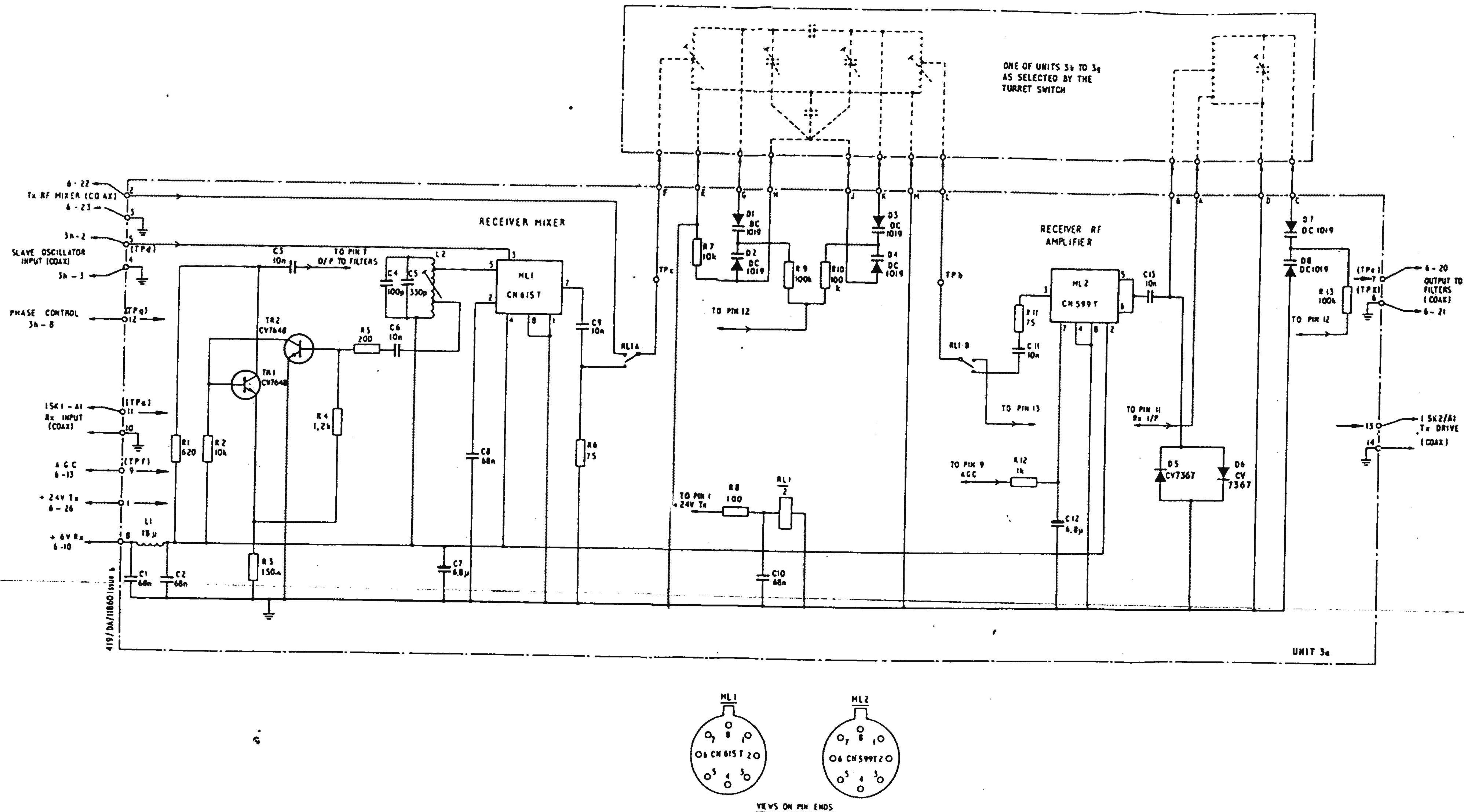
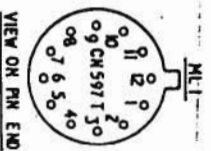
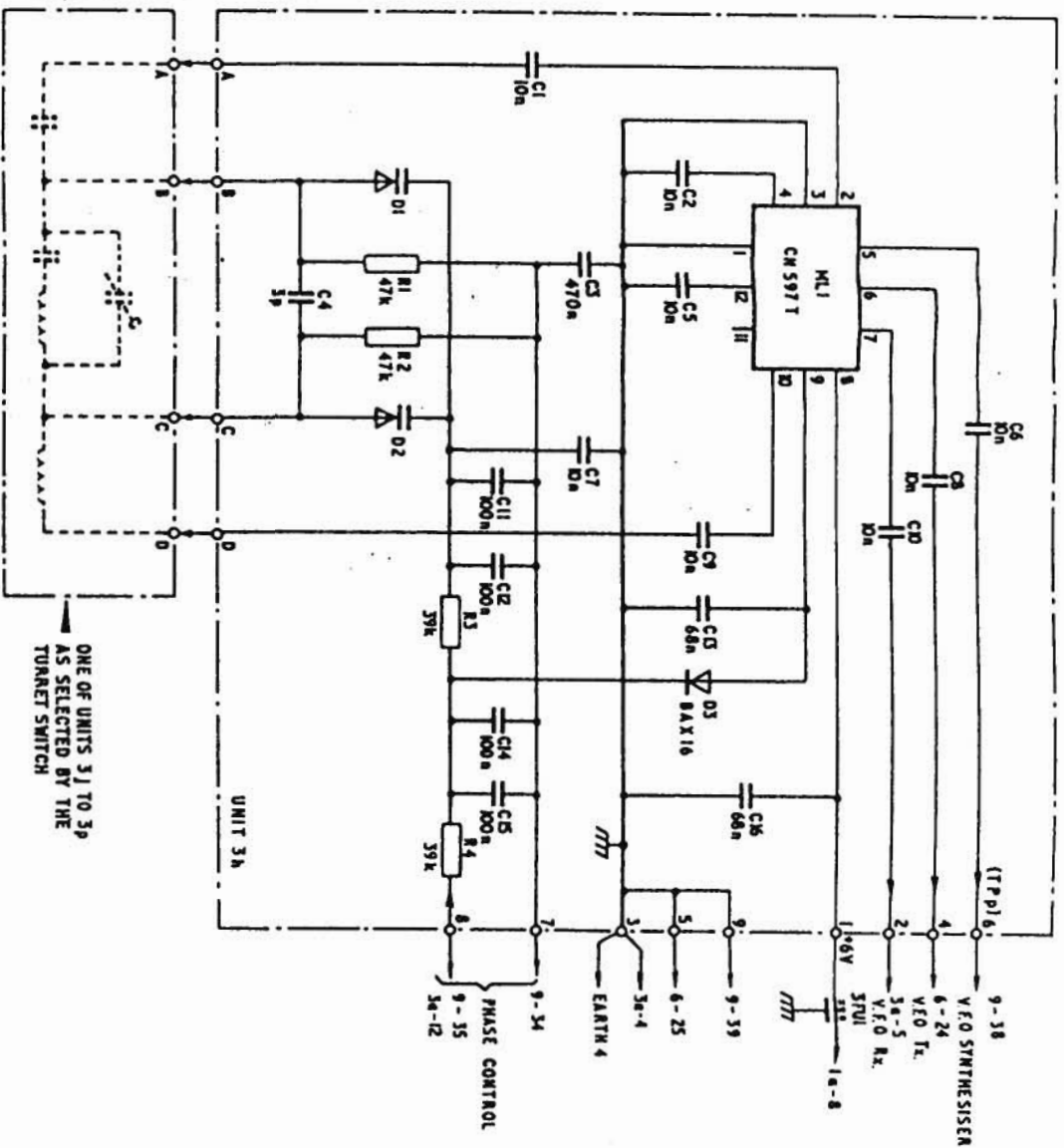


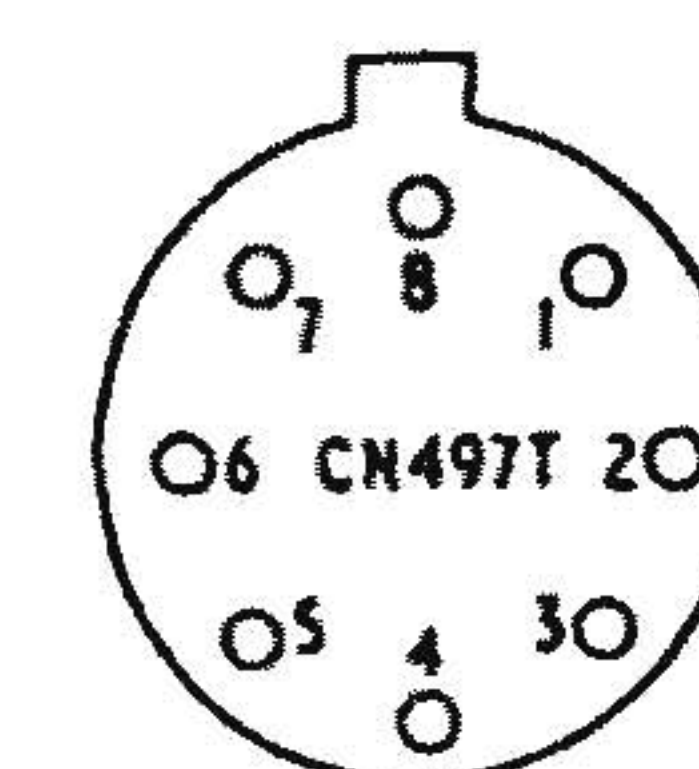
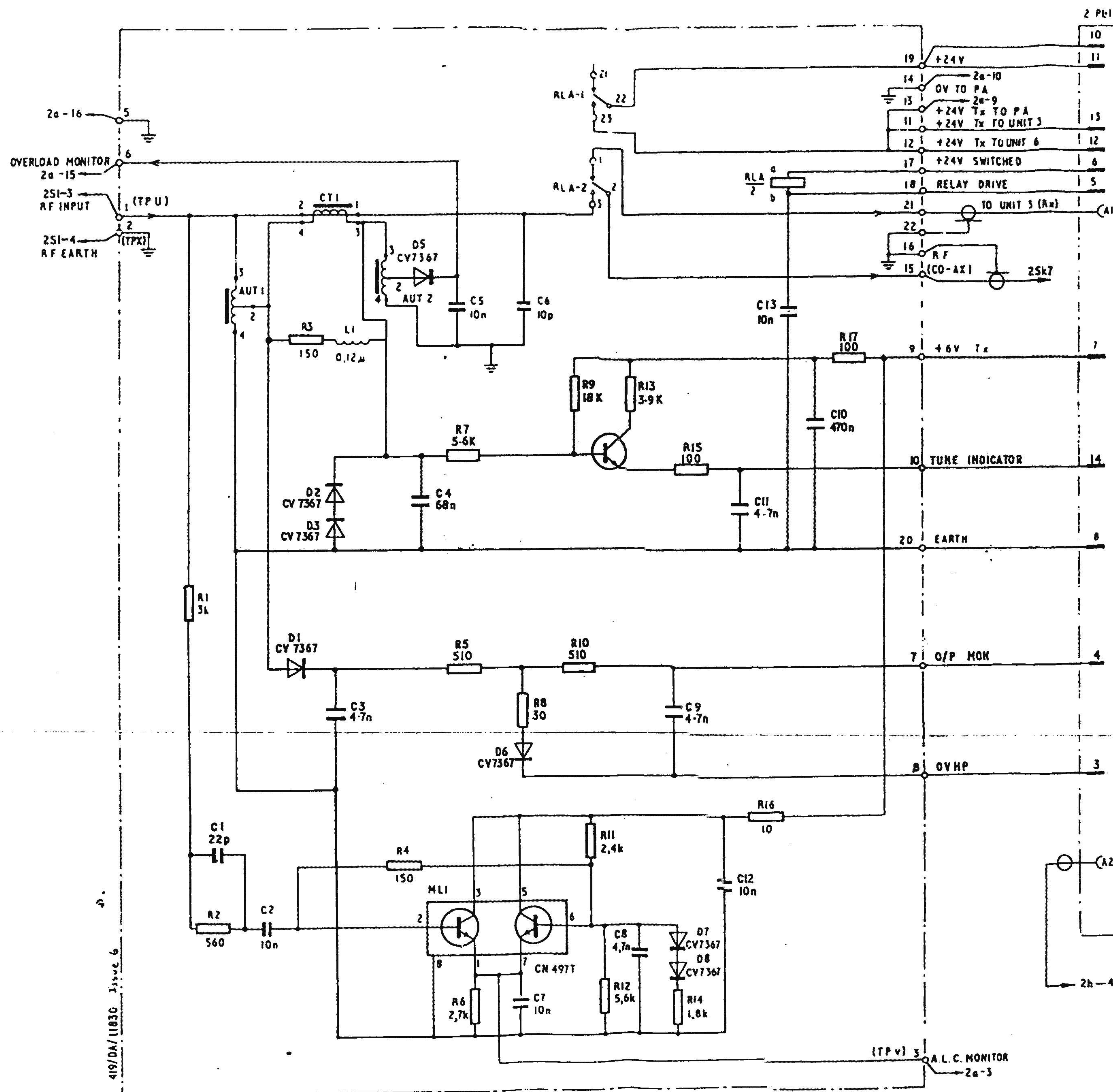
Fig. 1

Issue 1

R F Circuits



VFO - circuit



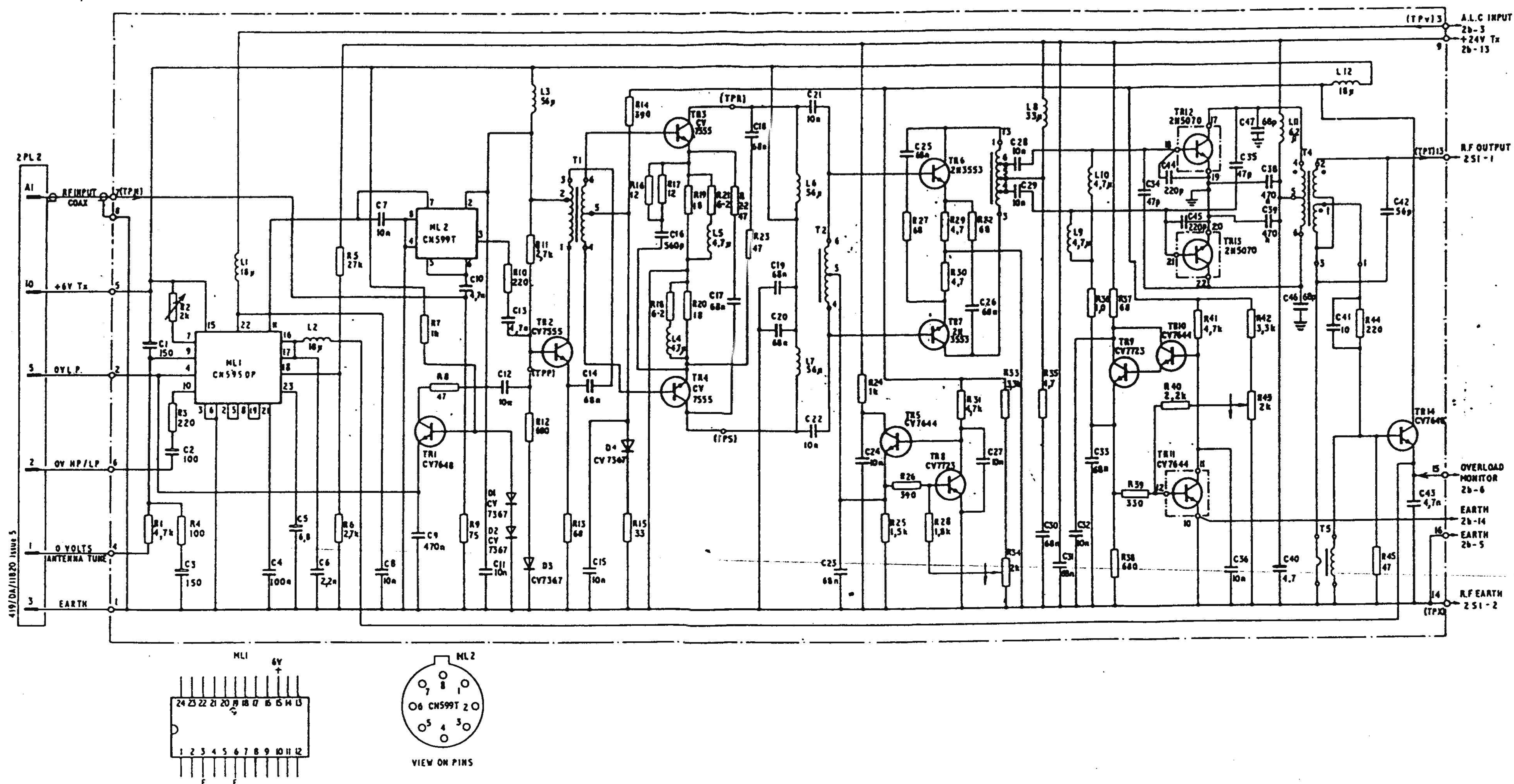


Fig. 2

Issue 1

Power amplifier (Unit 2a)-circuit

Fig.2

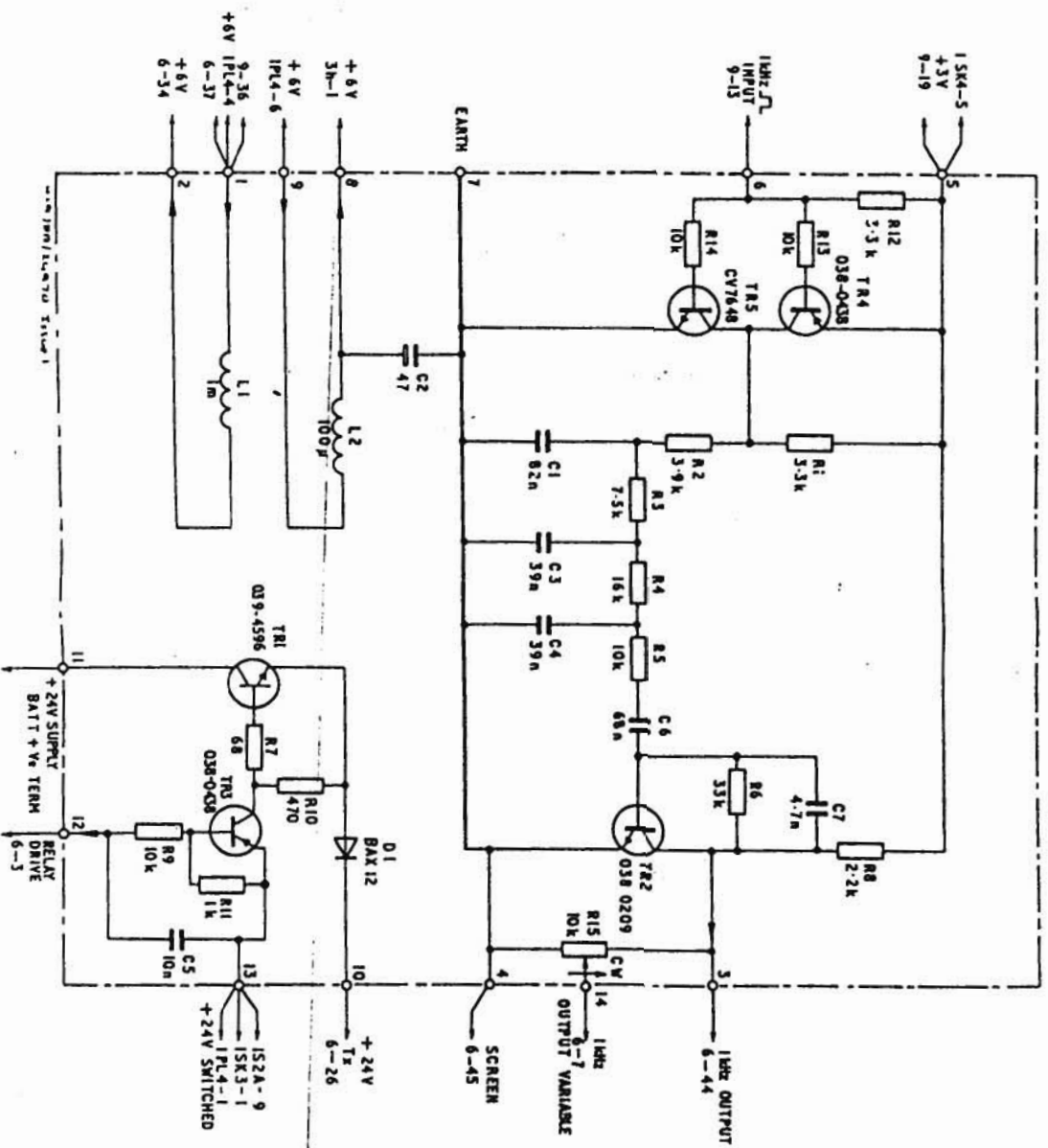


Fig. 1 1 kHz filter (Unit Ia) circuit diagram



Fig. 2
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